

STAFF PROFILE



NAME	Mr. RAGHURAMAN M	
DESIGNATION	LECTURER (CP)	
DEPARTMENT	ECE	
DATE OF JOINING IN THIS INSTITUTE	02.06.2014	
EDUCATIONAL QUALIFICATION(s)	M.E	
AREAS OF SPECIALIZATION(s)	DIGITAL ELECTRONICS	
EXPERIENCE		
Teaching: 10 Years	Industry: NIL	Total: 10 Years
FDP/WORKSHOPS/SEMINARS/ CONFERENCES ATTENDED	DIGITAL SIGNAL PROCESSING WORKSHOP	
CERTIFICATION COURSES	NIL	
PUBLICATIONS	An Area Efficient Hardware Design Of 128-bit Rijindael AES Algorithm Implementation with Full-Parallelism on FPGA	
RESPONSIBILITIES HELD	NIL	
Institutional Level:	<ul style="list-style-type: none">➤ ADMISSION COMMITTEE➤ ANNUAL DAY COMMITTEE➤ SPORTS COMMITTEE	
Department Level:	<ul style="list-style-type: none">➤ INTERNSHIP INCHARGE➤ TIME TABLE INCHARGE➤ CLASS ADVISOR➤ DISCIPLINE COMMITTEE	